

REMARKS

An Office Action was mailed April 30, 2010. This response is timely. Any fee due with this paper, including any necessary extension fees, may be charged on Deposit Account 50-1290.

Summary

Claims 1, 3, 11, 13, 21, and 25-28 are being examined. Claims 1, 11, 21, and 28 are the only independent claims that are being examined.

By the foregoing, claims 1, 11, 21, and 28 are amended. No new matter has been added.

Rejection under 35 U.S.C. §112

Claims 1, 3, 11, 13, 21, and 25-27 stand rejected under 35 U.S.C. §112, second paragraph as being indefinite for reciting "*the writing period*." The claims have now been amended to recite the subject matter more clearly. No new matter has been added. Accordingly, the Examiner is respectfully requested to withdraw the rejection.

Rejection under 35 U.S.C. §102(b) and 35 U.S.C. §103(a)

Claims 1, 3, 11, 13, and 28 stand rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 6,232,945 to Moriyama. Claims 25-27 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Moriyama in view of U.S. Patent No. 6,734,840 to Fukutofu. Independent claim 21 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Moriyama in view of JP 2001-249643 to Hirobumi. Thus, all claims stand rejected at least over Moriyama.

The rejections avers that "*the writing period*" and how the voltage is "*not inverted*" remain broad and undefined. Thus, the broadest possible interpretation is taken. Items 4 and 5 of the Office Action.

The rejection avers that the relationship between the polarity of a horizontal period and the polarity of a next horizontal period is not expressed in the claims. All independent claims now recite "*the polarity being oriented with respect to a vertical direction of the data lines.*" No new matter has been added.

As noted in the prior Response, Moriyama teaches "polarity;" for example, "in the same horizontal period", solid display data, e.g., black display, which includes a precharge case, and "the image display section may have the same polarity (i.e., the same polarity along the horizontal direction of the screen)", e.g., horizontal direction of the data lines.

However, Moriyama fails to disclose and teach the feature of the present invention that the relationship between the polarity of a horizontal period and the polarity of a next horizontal period, i.e., the same polarity along the vertical direction of the data lines. Unlike this, in Moriyama, "resetting operation" means that non-display data, e.g., a voltage for black display, is supplied to the image signal bus lines in accordance with the reset signal. The horizontal synchronizing period corresponds to the period from t0 to t3 in Moriyama. Therefore, the meaning of the resetting operation in the present invention is different from that of Moriyama.

Accordingly, the Examiner is respectfully requested to withdraw the rejections.

The rejection avers that resetting the outputs of the source driver circuit is not expressed in the claims. As previously presented, claims 1, 11, and 21 recite "*resetting the data voltages outputted by the source driver circuit.*" Claim 28 recites similar language.

As noted in the prior Response, in Moriyama, the video signal line driving circuit 291 outputs the non-display data for the vertical scanning period, so that the non-display data can be written in a plurality of the horizontal pixel lines. 14:16-19. This means that when the non-display data display areas 902 and 904 are respectively located at upper and lower positions of the display

data display area 902, the sampling pulses are outputted to the buffer amplified circuit 1007, so that the scanning voltage is outputted as shown in Fig. 16 in the vertical blanking period.

Thus, in Moriyama, the writing operation is carried out by selecting the non-display data from the source driver circuit, not by resetting the outputs of the source driver circuit.

Accordingly, the Examiner is respectfully requested to withdraw the rejections.

Independent claims 1, 11, and 21 now recite that

the writing period [is] a period where a data voltage is applied, the resetting operation comprising bringing values of all data voltages closer to the middle point voltage between the positive and negative amplitudes; and

wherein the data lines do not apply a subsequent data voltage to each of the pixels in the blanking period; the subsequent data voltage being a data voltage that follows a previous data voltage.

Therein, the writing period and how the voltage is applied is not taught by the cited art. No new matter has been added. In combination with the other amendments made herein, the claims are now definite and clearly read over Moriyama.

Accordingly, the Examiner is respectfully requested to withdraw the rejections.

In view of the remarks set forth above, this application is believed to be in condition for allowance which action is respectfully requested. However, if for any reason the Examiner should consider this application not to be in condition for allowance, the Examiner is respectfully requested to telephone the undersigned attorney at the number listed below prior to issuing a further Action.

Any fee due with this paper may be charged to Deposit Account No. 50-1290.

Respectfully submitted,

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